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ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

<p><i>M.H.</i></p> <p>O.I.P.E.</p> <p>SCANNED <i>30</i> O.A. <i>4/11</i></p>	PATENT DATE
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APPLICATION NO. 09/706728	CONT/PRIOR F	CLASS 380 713	SUBCLASS 193	ART UNIT 2131 2132	EXAMINER T. Swainy Colin
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TITLE **APPLICANTS**

Patrick Le Quere

2136

Architecture of an encryption circuit implementing various types of encryption algorithms simultaneously without a loss of performance

PTO-2040
12/99

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. 				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ 				ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
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